

TurboConcept

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TurboConcept is an SME located in Brest, France, and leader in providing innovative Intellectual Property Core on FPGA for digital transmissions.

We intend to bid for the task "Innovations by SME" (WP2423) on the theme "Innovative GNSS receiver using iterative (turbo) techniques". We could bring our expertise in algorithmic design & fast prototyping on FPGA of Turbo decoders (and jointequalization-detection-decoding) to an international team comprising a Polish SME.

Here is a short list of our credentials:

- 2000: First implementation of a duo-binary Turbo decoder for Eutelsat (DVB-RCS standard)
- 2001: First implementation of such a decoder for Alenia Spazio "Skyplex" onboard circuit
- 2002: Introduction of the most comprehensive offer of TPC (Turbo Product Code)
- 2003: Code proposal for DVB-S2 (we ranked second)
- 2003: First implementation of the DVB-S2 standard LDPC + BCH FEC
- 2004: Participation to several ESA ARTES-5 Projects
- Member of Newcom (EC NoE project), WiMAX and DVB

We could act indifferently as prime or as sub-contractor in such a consortium.